

# Notice of Allowability

Application No.

10/808,344

Examiner

Tuan Quach

Applicant(s)

KOYAMA, JUN

Art Unit

2826

## -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed 11/03/06.
2. ☒ The allowed claim(s) is/are 7-18 and 25-52.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
  1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

## Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

  
**Tuan Quach**  
**Primary Examiner**

## REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance:

Claims 7-18 and 25-52 are allowed primarily because the prior art does not teach all the claimed inventions in claims 7, 10, 13, 16, 33, 36, 39, 42. In particular, there is no teaching regarding a driver circuit comprising a shift register, a buffer circuit electrically connected thereto comprising a source follower circuit comprising a n-channel thin film transistor, and an analog memory electrically connected to the buffer circuit wherein a channel forming region of the thin film transistor comprises a polycrystalline semiconductor, the n-channel thin film transistor is a depletion mode transistor, and an active layer of the n-channel thin film transistor as characterized in claims 7, 10 last three lines. There is also no teaching regarding a driver circuit comprising a shift register, a buffer circuit electrically connected thereto comprising a bootstrap circuit comprising an n-channel thin film transistor, and an analog memory electrically connected to the buffer circuit wherein a channel forming region of the thin film transistor comprises a polycrystalline semiconductor, the n-channel thin film transistor is a depletion mode transistor, and an active layer of the n-channel thin film transistor as characterized in claims 13 and 16 last three lines. There is no teaching regarding a driver circuit comprising a shift register, a buffer circuit electrically connected thereto comprising a source follower circuit comprising a p-channel thin film transistor, and an analog memory electrically connected to the buffer circuit wherein a channel forming region of the thin film transistor comprises a polycrystalline

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semiconductor, the p-channel thin film transistor is a depletion mode transistor, and an active layer of the p-channel thin film transistor as characterized in claims 33, 36 last three lines. There is also no teaching regarding a driver circuit comprising a shift register, a buffer circuit electrically connected thereto comprising a bootstrap circuit comprising an p-channel thin film transistor, and an analog memory electrically connected to the buffer circuit wherein a channel forming region of the thin film transistor comprises a polycrystalline semiconductor, the p-channel thin film transistor is a depletion mode transistor, and an active layer of the p-channel thin film transistor as characterized in claims 39 and 42 last three lines. There also are no suggestion to modify and combine the various separate prior art references to arrive at all the claimed features of the inventions as delineated.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to examiner Tuan Quach whose telephone number is 571-272-1717. The examiner can normally be reached on M-F from 8:30 AM to 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor Nathan Flynn, can be reached on 571-272-1915. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



**Tuan Quach**  
**Primary Examiner**